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6. Design Methodology for Voltage-Scaled Clock Distribution Networks-MARCH 2016
7. A Test Selection Procedure for Improving the Accuracy of Defect Diagnosis- JUNE 2016
9. 5-bit 5-GS/s Noninterleaved Time-Based ADC in 65-nm CMOS for Radio-Astronomy Applications MAY 2016
10. Enhanced Built-In Self-Repair Techniques for Improving Fabrication Yield and Reliability of Embedded Memories - FEB 2016
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25. Low-Latency ECDSA-Signature Verification - A Road Toward Safer Traffic - MAY 2016

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30. A Fault Tolerance Technique for Combinational Circuits Based on Selective-Transistor Redundancy-MAY 2016
31. Study and Analysis of the Effects of SiGe Source and Pocket-Doped Channel on Sensing Performance of Dielectrically Modulated Tunnel FET-Based Biosensors-MAY 2016
34. Time-Based Sensor Interface Circuits in CMOS and Carbon Nanotube Technologies-MARCH 2016
36. More Power Reduction with 3-Tier Logic-on-Logic 3D ICs-APRIL 2016
37. Efficient Designs of Multiported Memory on FPGA-JUNE 2016
38. Asymmetrical Dead-Time Control Driver for Buck Regulator-APRIL 2016
41. Parallel High-Order Envelope-Following Method for Fast Transient Analysis of Highly Oscillatory Circuits-MAY 2016
42. Scalable Approach for Power Droop Reduction During Scan-Based Logic BIST-JUNE 2016
44. Nonlinearity Estimation for Compensation of Phase Interpolator in Bang-Bang CDRs-JUNE 2016
45. Floorplanning Automation for Partial-Reconfigurable FPGAs via Feasible Placements Generation-MAY 2016
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46. Temporarily Fine-Grained Sleep Technique for Near- and Subthreshold Parallel Architectures-JUNE 2016
47. A 5-Gb/s Digital Clock and Data Recovery Circuit With Reduced DCO Supply Noise Sensitivity Utilizing Coupling Network-MAY 2016
50. Defragmentation for Efficient Runtime Resource Management in NoC-Based Many-Core Systems-APRIL 2016
54. Timing Analysis of Tasks on Runtime Reconfigurable Processors-JUNE 2016
56. A Highly Scalable Optical Network-on-Chip With Small Network Diameter and Deadlock Freedom-MAY 2016
60. System-Level Power Analysis of a Multicore Multipower Domain Processor With ON-Chip Voltage Regulators-MAY 2016
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62. Optimizing the Implementation of SEC-DAEC Codes in FPGAs-MAY 2016
63. Metastability in SAR ADCs-APRIL 2016
64. Efficiency Optimization of Silicon Photonic Links in 65-nm CMOS and 28-nm FDSOI Technology Nodes-APRIL 2016
65. All-Digital ON-Chip Process Sensor Using Ratioed Inverter-Based Ring Oscillator-MAY 2016
67. An Efficient Component for Designing Signed Reverse Converters for a Class of RNS Moduli Sets of Composite Form \(2k,2p - 1\)-JUNE 2016
68. Exploiting Intracell Bit-Error Characteristics to Improve Min-Sum LDPC Decoding for MLC NAND Flash-Based Storage in Mobile Device-APRIL 2016
70. Systematic Methodology for the Quantitative Analysis of Pipeline-Register Reliability-JUNE 2016
71. EBSCam: Background Subtraction for Ubiquitous Computing-MAY 2016
73. An Efficient Eligible Error Locator Polynomial Searching Algorithm and Hardware Architecture for One-Pass Chase BCH Code Decoding-JUNE 2016
75. Architecture of Ring-Based Redundant TSV for Clustered Faults-MAY 2016
76. A Cache-Assisted Scratchpad Memory for Multiple-Bit-Error Correction-APRIL 2016
77. Analyzing the Effect of Clock Jitter on Self-Oscillating Sigma Delta Modulators-JAN 2016
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78. Hypergraph Based Minimum Arborescence Algorithm for the Optimization and Reoptimization of Multiple Constant Multiplications - JAN 2016
81. Low-Power FPGA Design Using Memoization-Based Approximate Computing - FEB 2016
82. Exploiting Intracell Bit-Error Characteristics to Improve Min-Sum LDPC Decoding for MLC NAND Flash-Based Storage in Mobile Device - APR 2016
83. A Normal I/O Order Radix-2 FFT Architecture to Process Twin Data Streams for MIMO - JAN 2016
84. Frequency-Boost Jitter Reduction for Voltage-Controlled Ring Oscillators - APR 2016